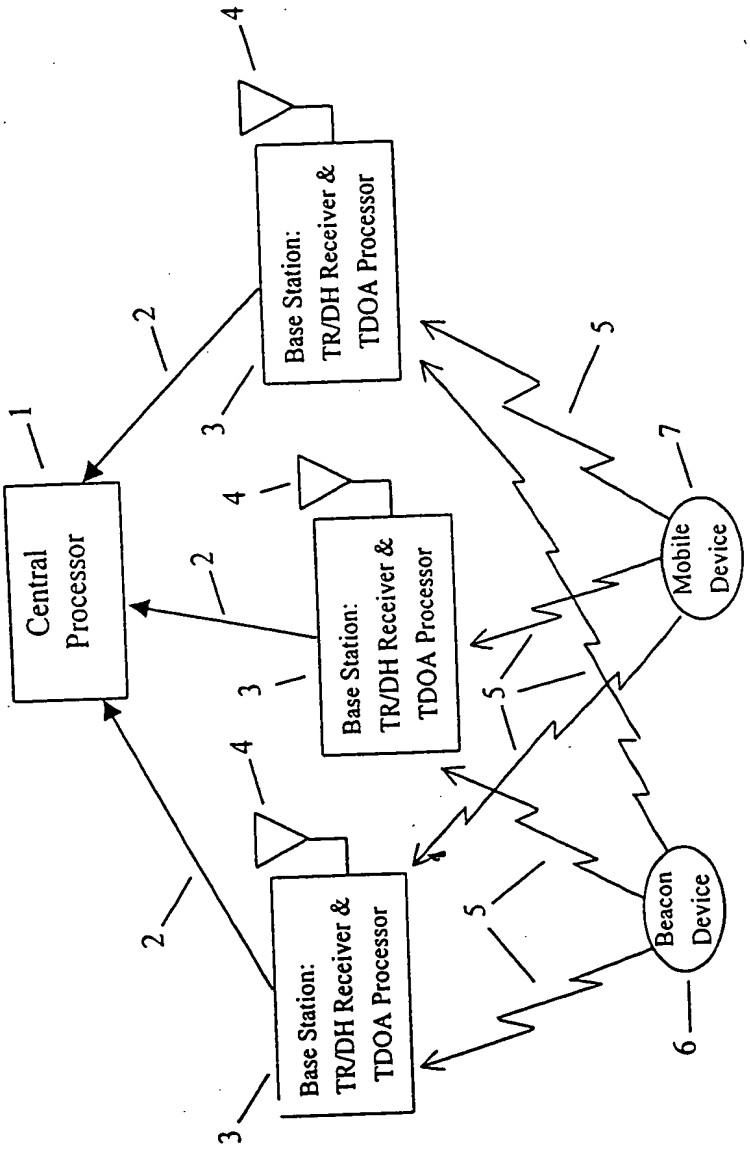
Central Processor
Base Station: TR/DH Receiver & TDOA Processor
Base Station: TR/DH Receiver & TDOA Processor
Base Station: TR/DH Receiver & TDOA Processor
Mobile Device
Beacon Device
1
2
3
4
5
6
7

Fig. 1

Serial No.:
Inventor: R. Hoctor et al.
Docket No.: RD-27,855
Attorney: J. Breedlove
Atty's Tele. No.: 518.387.6276

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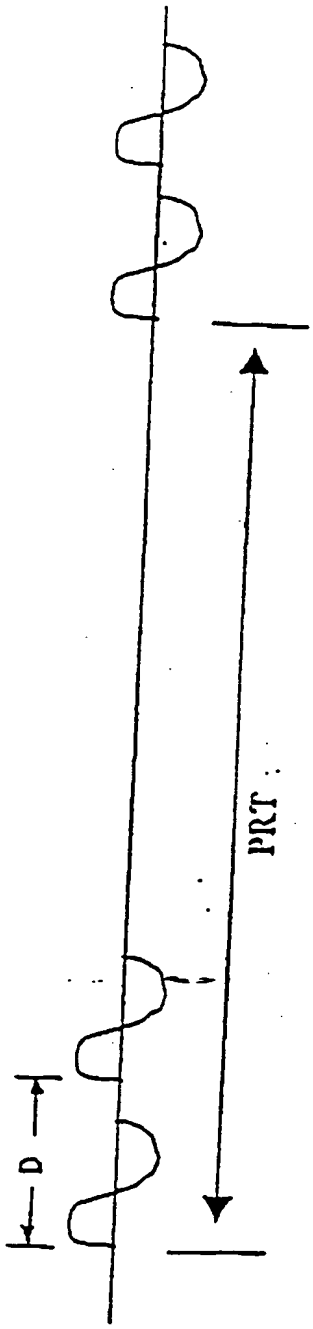

Fig. 2

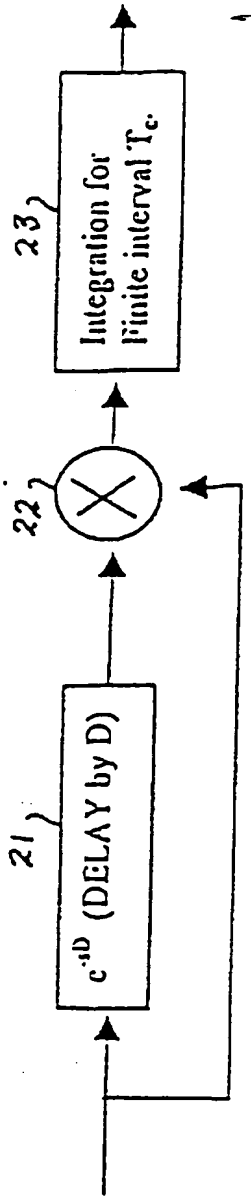

Fig. 3

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N_p pulse pairs separated by time D_1, with data bit B_1.	N_p pulse pairs separated by time D_2, with data bit B_2.	. . .	N_p pulse pairs separated by time $D_{(Nc-1)}$, w/ data bit $B_{(Nc-1)}$	N_p pulse pairs separated by time D_{Nc}, with data bit B_{Nc}.

Fig. 4

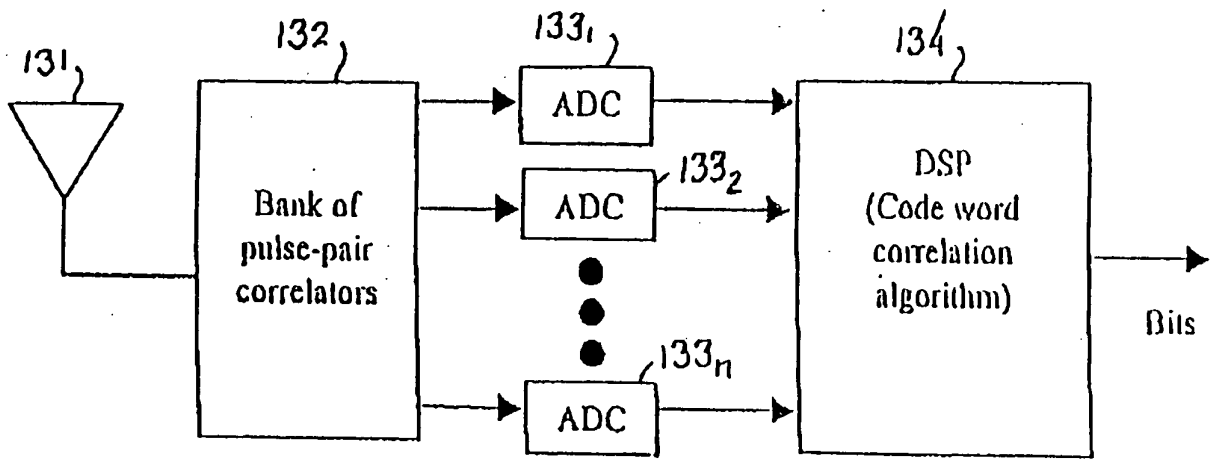

Fig. 5

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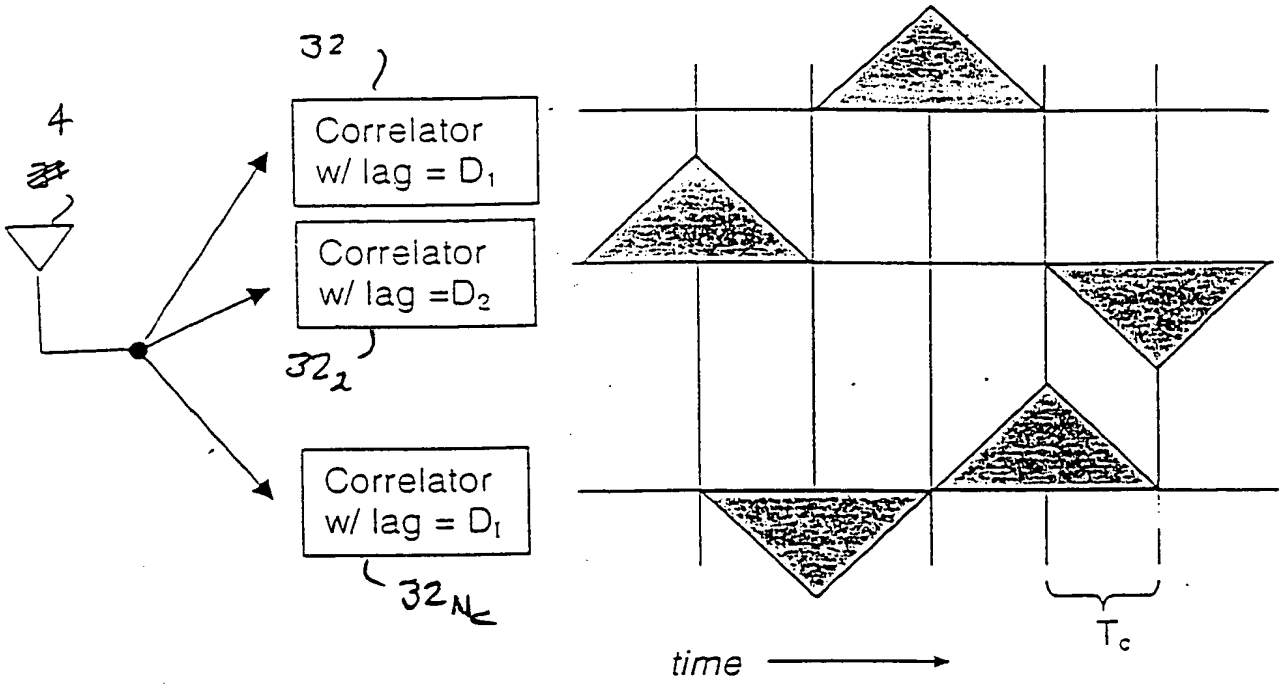
32
4
Correlator w/ lag = D_1
Correlator w/ lag = D_2
Correlator w/ lag = D_L
32_2
32_L
T_c
time

FIG. 6

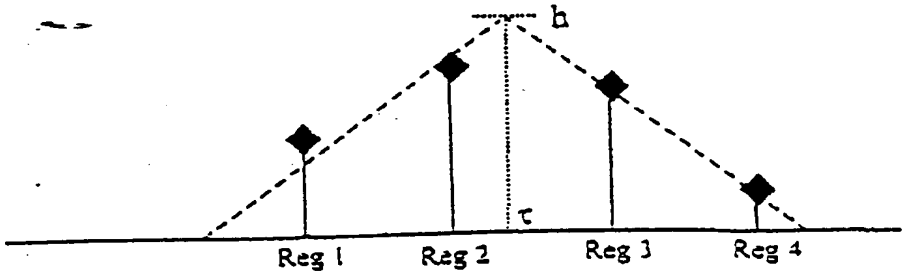
h
τ
Reg 1
Reg 2
Reg 3
Reg 4

FIG. 7

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Inventor: R. Hoctor et al.
Docket No.: RD-27,855
Attorney: J. Breedlove
Atty's Tele. No.: 518.387.6276

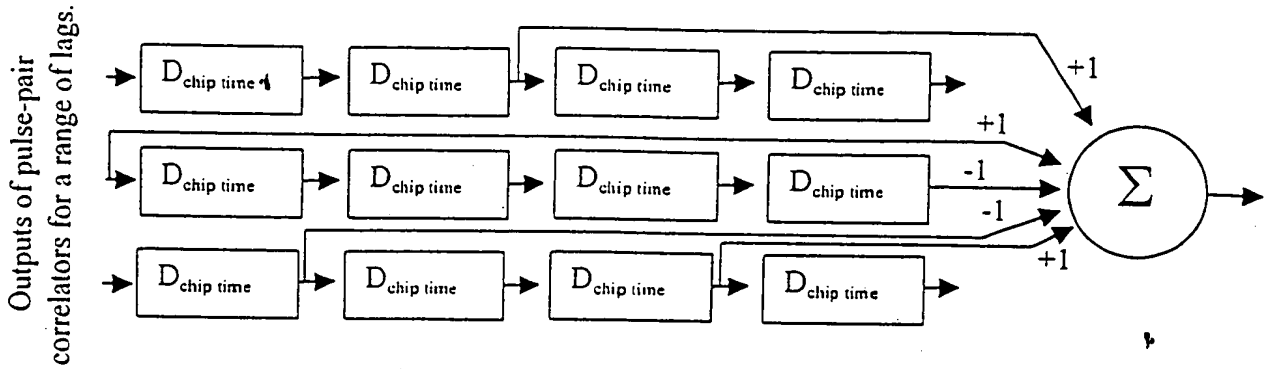

Fig. 8

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